

# Product utilizing with SESUB Technology

*[SESUB : Semiconductor Embedded in SUBstrate]*

## **Bluetooth V4.0 Low Energy Module**

Type : SESUB-PAN-T2541

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## Features

### ● General

- Bluetooth V4.0 Low Energy Module, its size of 4.6mm x 5.6mm x 1.0mm, only possible using TDK proprietary SESUB technology embedding Texas Instruments CC2541 semiconductor and integrating the function required components.
- Space saving, <math>26\text{mm}^2</math> total package size compared with  $36\text{mm}^2</math> as discrete solution.$
- Module Height of 1.0mm (typ).
- 36 user pins with center GND pads, 0.5mm pad pitch with solder bumped LGA.
- Complete module solution allowing just plug and play to the antenna, giving the designers more flexibility in small area solutions
- All development tools provided from Texas Instruments for discrete CC2541 package IC can be used for this module without any modification.
- Embedded Bluetooth-Stack from Texas Instruments available.

### ● RF

- 2.4GHz Bluetooth Low Energy Mode.
- Programmable Output Power up to 0 dBm.
- Accurate Digital RSSI Support.

### ● Microcontroller

- High-Performance and Low-Power 8051 Microcontroller Core with Code Prefetch.
- 256KB of In-System-Programmable Flash.
- 8KB RAM.

### ● Peripherals

- Five-Channel DMA.
- General-Purpose Timers (One 16-bit, Two 8-bit).
- I2C Interface.
- 32kHz Sleep Timer with Capture.
- Battery Monitor and Temperature Sensor.
- 12-bit ADC with Eight channels and Configurable Resolution.
- AES Security Coprocessor.
- Two UARTs with supporting for Several Serial Protocols.
- Total 23 General Purpose I/O Pins (21 x 4mA, 2 x 20mA output current capable).
- Watchdog Timer.
- Integrated High-Performance Comparator.

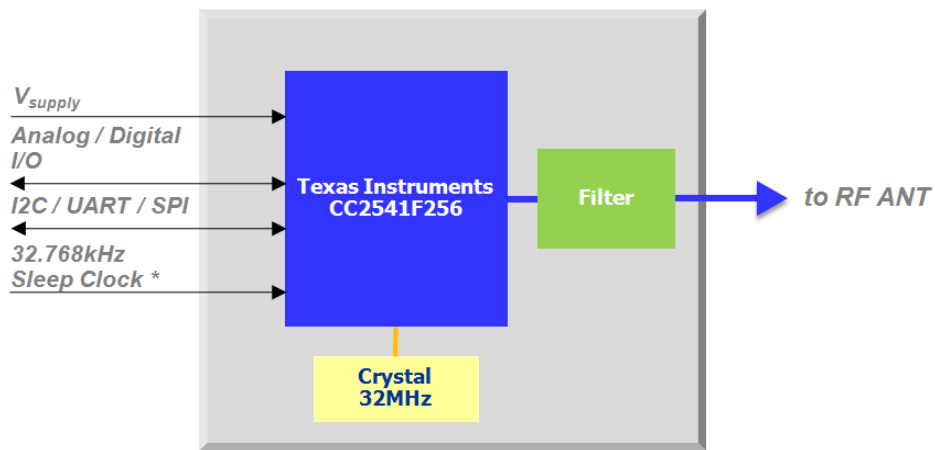
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● **Applications**

- Human-Interface Devices (Keyboard, Mouse, Remote Control).
- Sports and Leisure Equipment.
- Fitness / Healthcare Products
- Sensor Monitoring Products
- Gaming
- Wearable Products
- Mobile Phone Accessories
- Digital Consumer Electronics

**Block Diagram**

The module block diagram is shown in Figure 1.



**Figure 1 Module Block Diagram**

Remark \*

External 32.768 kHz crystal or external 32.768 kHz clock input from other device can be used.

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# Schematic

The module Schematic is shown in Figure 2.

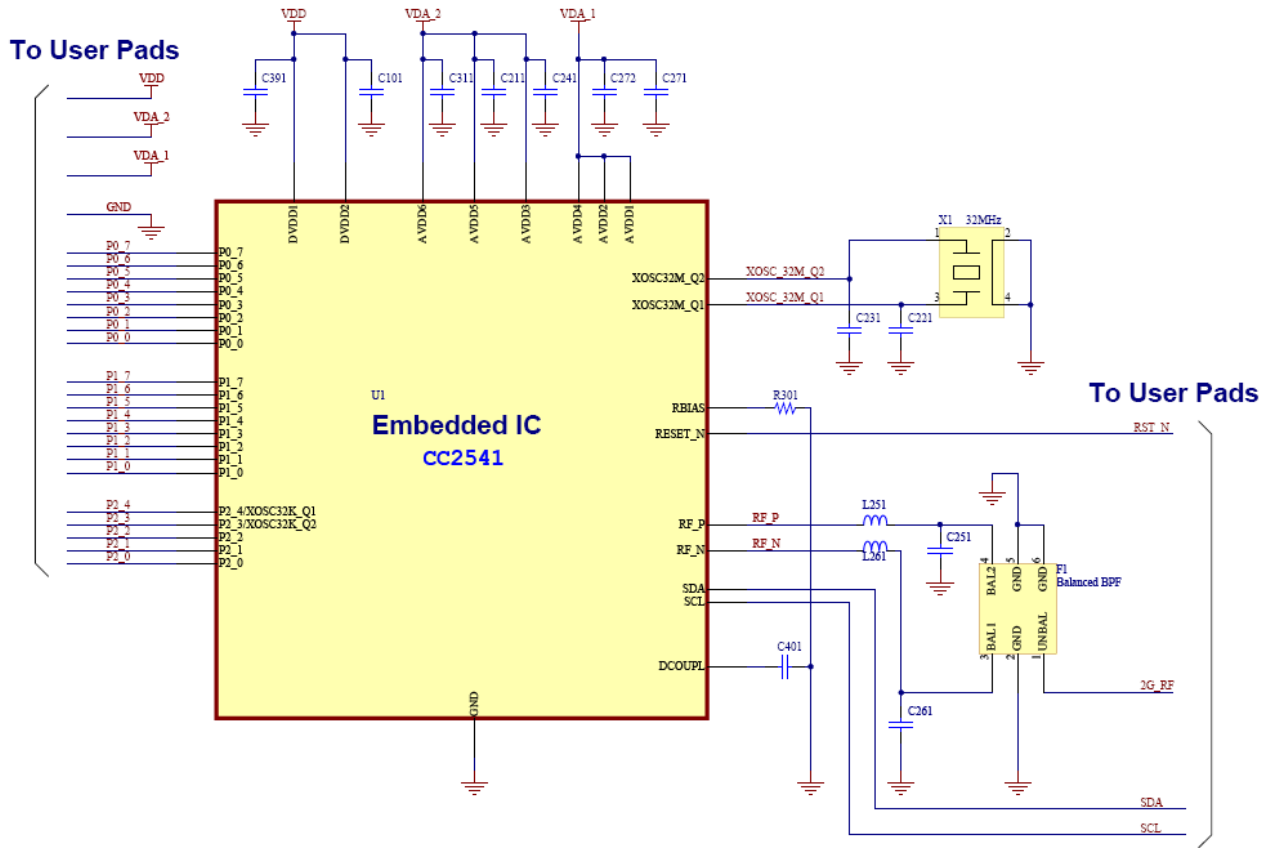


Figure 2 Module Schematic

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## Absolute Maximum Ratings

Over operating room temperature range (unless otherwise noted)

Item	VALUE	UNIT
Supply voltage range	-0.3 to 3.9	V
Voltage range to any of digital pins	-0.3 to VDD + 0.3, <3.9	V
Storage temperature range	-40 to +85	°C
Radio input power level	+10	dBm
ESD (Charged Device Model)	500	V
ESD (Human Body Model)	1000	V

## Operating Conditions

Over operating room temperature range (unless otherwise noted)

Item	Min	Typ	Max	UNIT
Supply voltage range	2.0		3.6	V
Operational temperature range	-20		+70	°C

## Electrical Characteristics

Measured on SP13801 EM board with the condition of Ta=25°C±10°C and VDD = 3V

PARAMETER	Conditions	Value			UNIT
		Min	Typ	Max	
Rx mode Current	RF input PWR level: -70dBm Packet type: RF_PHY_TEST Payload Data Pattern: PRBS9 Payload Data Length: 37bytes		19.8		mA
Tx mode Current	RF output PWR level: 0dBm Payload Data Pattern: PRBS9 Payload Data Length: 37bytes		20.6		mA
Standby mode Current	PM2 (Sleep Timer On) <i>refer IC datasheet in detail</i>		1.2		µA

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## General RF Characteristics

Measured on SP13801 EM board with the condition of  $T_a=25^{\circ}\text{C}\pm 10^{\circ}\text{C}$  and  $V_{DD} = 3\text{V}$

PARAMETER	Conditions	Value			UNIT
		Min	Typ	Max	
Center Frequency		2402		2480	MHz
Channel Spacing			2		MHz
RF Port Impedance			50		Ohm

## RF Characteristics

Measured on SP13801 EM board with the condition of  $T_a=25^{\circ}\text{C}\pm 10^{\circ}\text{C}$  and  $V_{DD} = 3\text{V}$

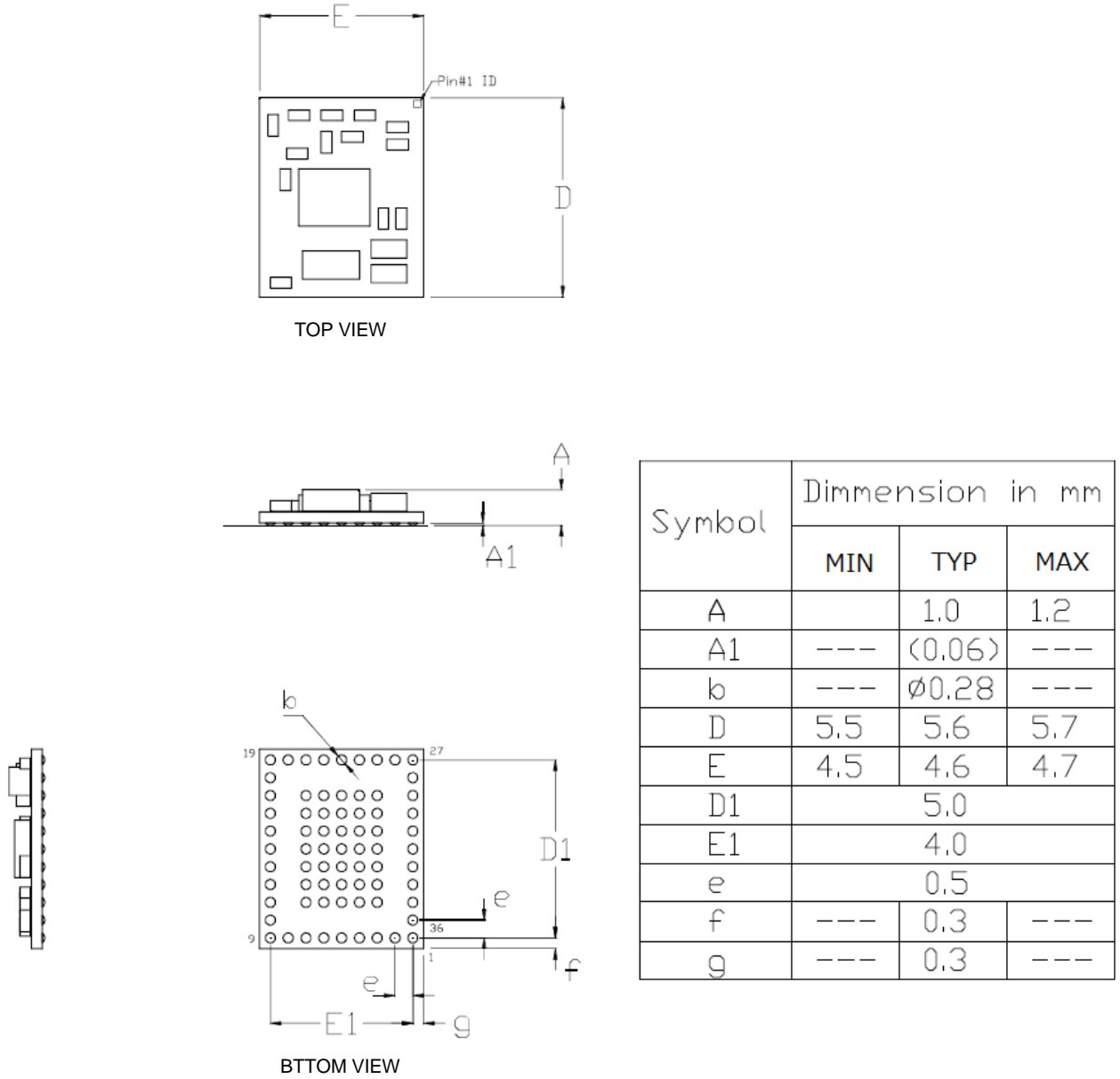
$f_c=2440\text{MHz}$

PARAMETER	Conditions	Value			UNIT
		Min	Typ	Max	
Tx Output Power	Maximum output power setting	-6	0	-	dBm
Tx Frequency accuracy	Maximum output power setting XOSC32M_TUNE[3:0]= 0x0F	-150	0	+150	kHz
Tx modulation characteristics					
Delta F1		225		275	kHz
Delta F2/F1		0.8			-
Delta F2 Max		99.9			%
Receiver Sensitivity max input level [measured by Packet Error Rate]	Packet error rate : $\leq 30.8\%$	-10			dBm
Receiver Sensitivity level [measured by Packet Error Rate]	Packet error rate : $\leq 30.8\%$			-70	dBm

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# Mechanical Dimensions

The module mechanical dimensions are shown in Figure 3.



**Figure 3 Module Mechanical Dimensions**

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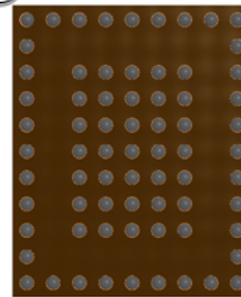
## Module Pin-Out

The module Pin-Out and names are shown in Figure 4 and Table 1.

1	2	3	4	5	6	7	8	9
GND	P2_1	P2_0	VDD	VDA_1	VDA_2	GND	RST_N	GND
36								10
P2_2								P0_0
35								11
GND		GND	GND	GND	GND	GND		P0_1
34								12
P2_4/32k		GND	GND	GND	GND	GND		P0_2
33								13
P2_3/32k		GND	GND	GND	GND	GND		P0_3
32								14
P1_7		GND	GND	GND	GND	GND		P0_4
31								15
P1_6		GND	GND	GND	GND	GND		P0_5
30								16
P1_1		GND	GND	GND	GND	GND		P0_6
29								17
P1_2		GND	GND	GND	GND	GND		P0_7
28								18
P1_3								P1_0
27	26	25	24	23	22	21	20	19
GND	SCL	SDA	P1_4	P1_5	GND	2G_RF	GND	GND

RF	Yellow
Power	Red
Clock	Orange
I/O	White
Cont	Light Green
GND	Blue

Pin 1



**Module Bottom View**

**Figure 4 Module Pin-Out & Color Definition**

**Table 1 PIN Descriptions**

Pin Nr	Pin Name	Pin Type	Description
1	GND	Ground	Connect to Ground
2	P2_1/DD	Digital I/O	Port2.1 / Programming I/F DD
3	P2_0	Digital I/O	Port2.0
4	VDD	Power	2V-3.6V digital power supply
5	VDA_1	Power	2V-3.6V analog power supply
6	VDA_2	Power	2V-3.6V analog power supply
7	GND	Ground	Connect to Ground
8	RST_N	Digital I/O	Reset in active low
9	GND	Ground	Connect to Ground
10	P0_0	Digital I/O	Port0.0
11	P0_1	Digital I/O	Port0.1
12	P0_2	Digital I/O	Port0.2 / UART RX / SPI MISO
13	P0_3	Digital I/O	Port0.3 / UART TX / SPI MOSI
14	P0_4	Digital I/O	Port0.4 / UART CTS / SPI CS
15	P0_5	Digital I/O	Port0.5 / UART RTS / SPI CLK
16	P0_6	Digital I/O	Port0.6

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Pin Nr	Pin Name	Pin Type	Description
17	P0_7	Digital I/O	Port0.7
18	P1_0	Digital I/O	Port1.0 – 20mA drive capability
19	GND	Ground	Connect to Ground
20	GND	Ground	Connect to Ground
21	2G_RF	RF I/O	Connect to RF antenna
22	GND	Ground	Connect to Ground
23	P1_5	Digital I/O	Port1.5
24	P1_4	Digital I/O	Port1.4
25	SDA	Digital I/O	I2C SDA // Leave floating if not used.
26	SCL	Digital I/O	I2C SCL // Leave floating if not used.
27	GND	Ground	Connect to Ground
28	P1_3	Digital I/O	Port1.3
29	P1_2	Digital I/O	Port1.2
30	P1_1	Digital I/O	Port1.1 – 20mA drive capability
31	P1_6	Digital I/O	Port1.6
32	P1_7	Digital I/O	Port1.7
33	P2_3/32k-1	I/O / Clock	Port2.3 / 32.768kHz crystal1
34	P2_4/32k-2	I/O / Clock	Port2.4 / 32.768kHz crystal2
35	GND	Ground	Connect to Ground
36	P2_2/DC	Digital I/O	Port2.2 / Programming I/F DC

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**Table 2 Peripheral I/O Pin Mapping**

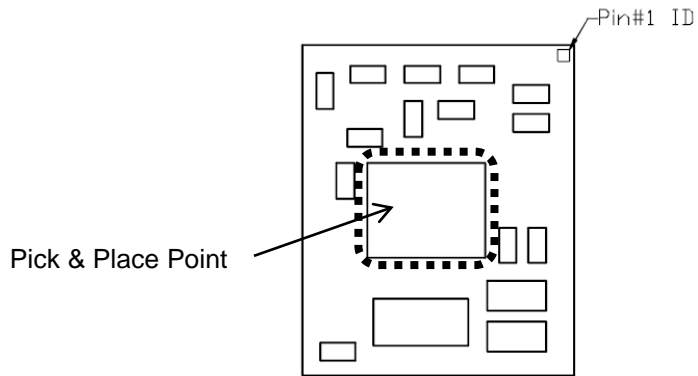
Periphery/ Function	P0								P1								P2				
	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	4	3	2	1	0
ADC	A7	A6	A5	A4	A3	A2	A1	A0													T
Operational amplifier						O	-	+													
Analog comparator			+	-																	
USART 0 SPI			C	SS	MO	MI															
Alt. 2											MO	MI	C	SS							
USART 0 UART			RT	CT	TX	RX															
Alt. 2											TX	RX	RT	CT							
USART 1 SPI			MI	MO	C	SS															
Alt. 2									MI	MO	C	SS									
USART 1 UART			RX	TX	RT	CT															
Alt. 2									RX	TX	RT	CT									
TIMER 1		4	3	2	1	0															
Alt. 2	3	4												0	1	2					
TIMER 3												1	0								
Alt. 2									1	0											
TIMER 4															1	0					
Alt. 2																		1			0
32-kHz XOSC																	Q1	Q2			
DEBUG																			DC	DD	
OBSSEL											5	4	3	2	1	0					

Notes:

This table is referred from the “CC2541 User Guide” (swru191). See description about I/O Pins in section 7. SESUB-PAN-T2541 has same I/O Pin structure with CC2541 SoC.

## Module Pick & Place Point

This module can be handled at the area where shown in red colored break during assembling.  
The area is 2.0x1.6mm. (Picking up with top of the crystal Unit)



**Figure 6 Module Pick & Place Point**

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# Marking

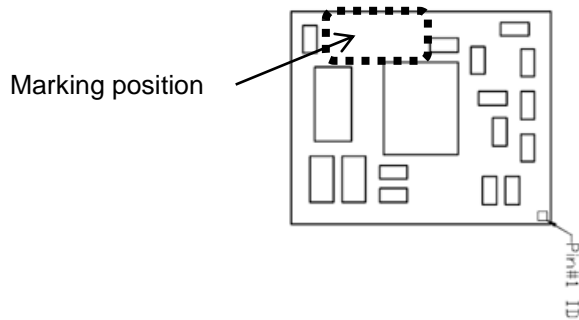
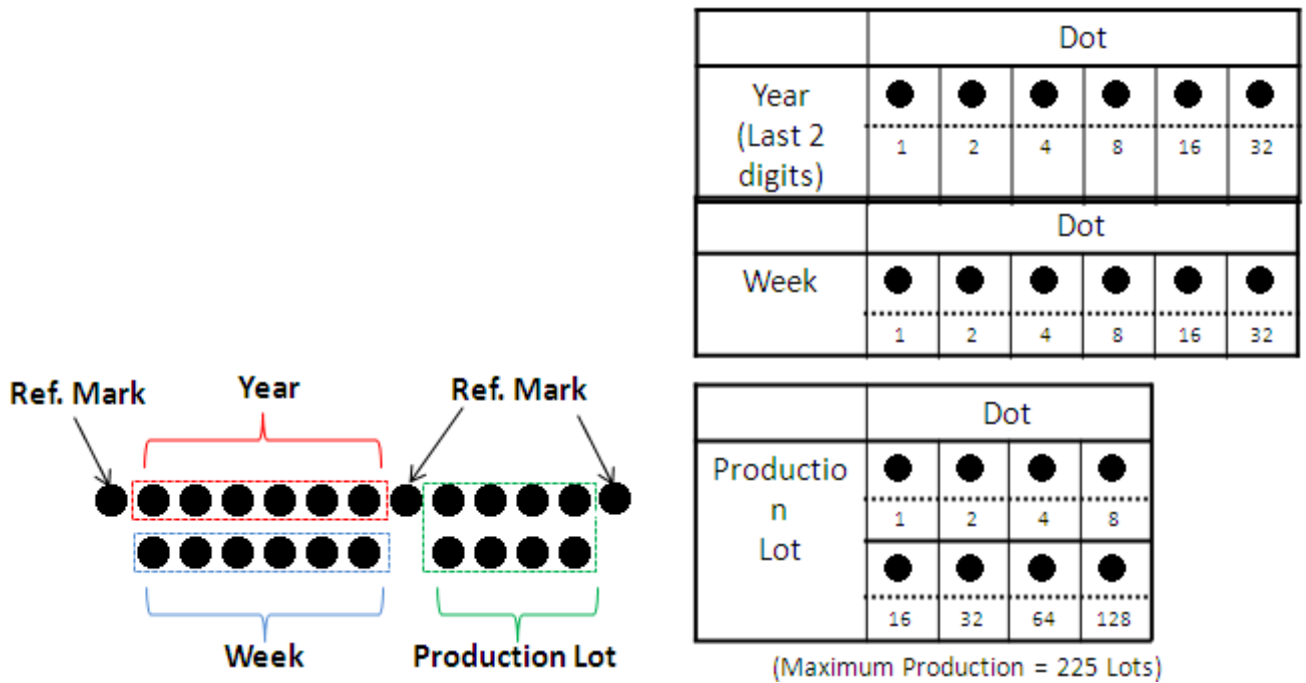


Figure 7 Module Marking Position



## Example

Year : 2013

Week : 45

Production Lot : 25

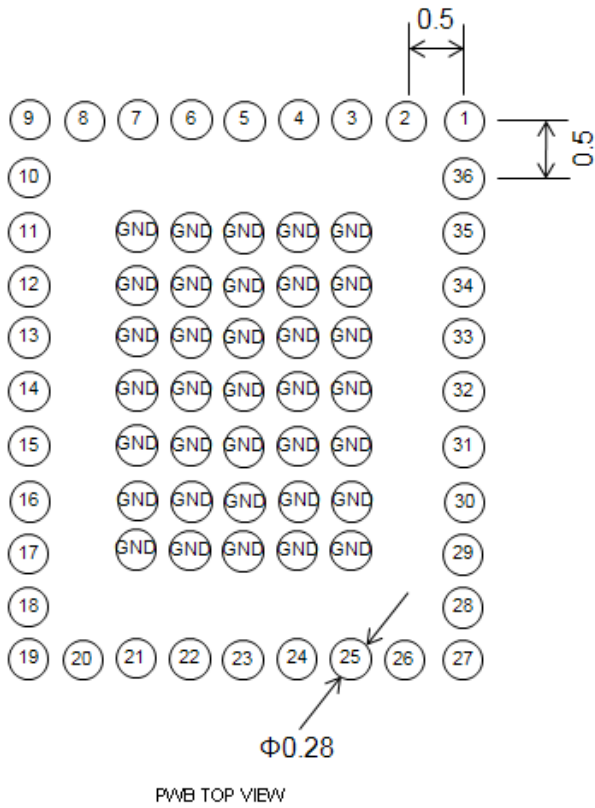


Figure 8 Module lot code

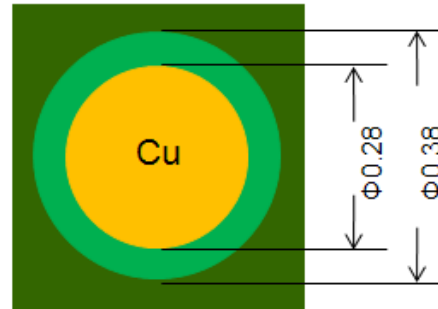
<b>NAME OF DRAWING</b>	<b>DRAWING NO</b>	<b>DATE</b>	<b>REV</b>	<b>PAGE</b>
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## Recommended PWB Footprint

The recommended PWB foot print for the module is shown in Figure 9.



### Pad Opening Definition



### Non-Solder Mask Defined

#### Notice

Module user pads have solder bumps on it. Pre-soldering is required on customer PCB pads for the module.

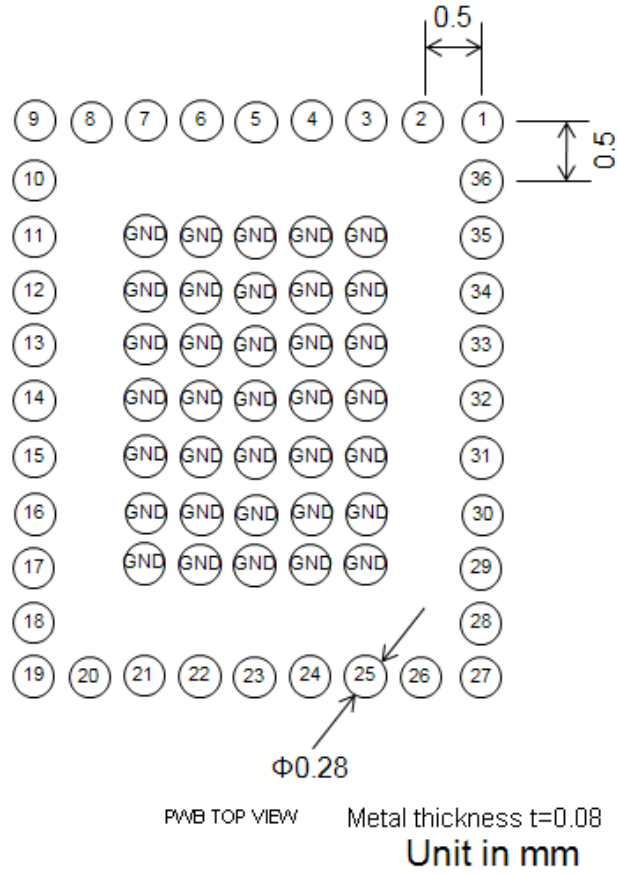
Unit in mm

Figure 9 Recommended PWB Footprints

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## Recommended Metal Mask Design

The recommended metal mask design for the module is shown in Figure 10.



**Figure 10 Recommended Metal Mask Design**

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## Recommended Reflow Profile

The recommended reflow profile for the module is shown in Figure 11.

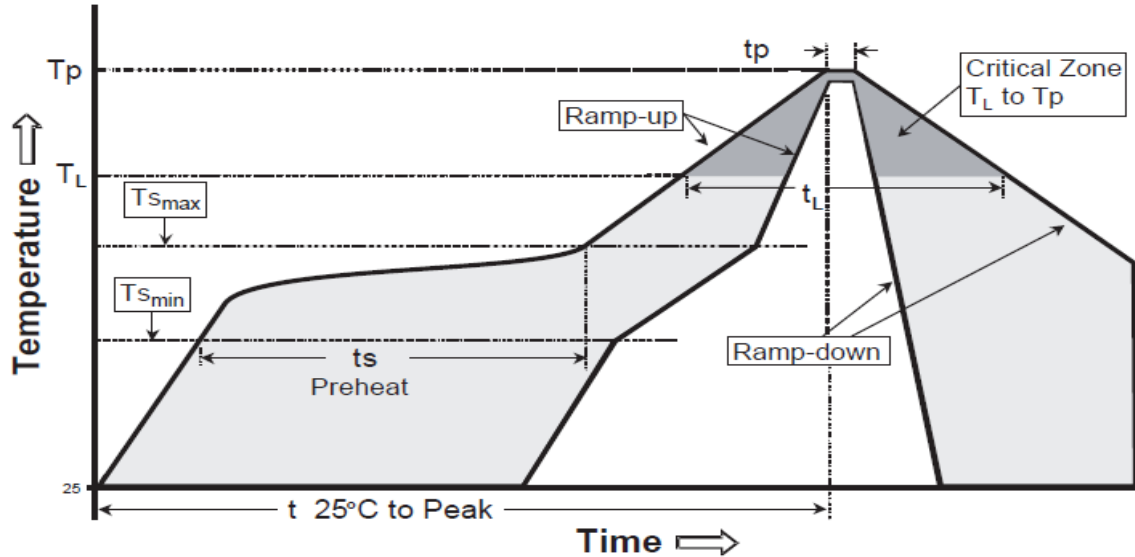


Figure 11 Recommended Reflow Profile

**Table 4 Reflow Profile Condition**

Profile Feature	Range
Average Ramp-Up Rate ( $T_{s_{max}}$ to $T_p$ )	0.8 °C/seconds
Preheat:	
-Temperature Min ( $T_{s_{min}}$ )	150 °C
-Temperature Max ( $T_{s_{max}}$ )	180 °C
-Time ( $t_{s_{min}}$ to $t_{s_{max}}$ )	100 +/-10 seconds
Time maintained above:	
-Temperature ( $T_L$ )	220 °C
-Time ( $t_L$ )	50 +/-10 seconds
Peak Temperature ( $T_p$ )	250 °C
Time within 5°C of actual Peak Temperature ( $t_p$ )	20-40 seconds
Ramp-Down Rate	6 °C/seconds max.
Time 25°C to Peak Temperature	8 minutes max.

Note:

Solder material used in this product: M705-GRN260K2KJ-V (Senjyu Metal Industry Co., Ltd.)

Permissible maximum reflow cycle: 2

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## Tape & Reel Specifications

Reel Diameter: 180 mm  
 Reel Width 13.0mm  
 Reel Hub Diameter: 21mm  
 Qty/reel: 1000 pcs

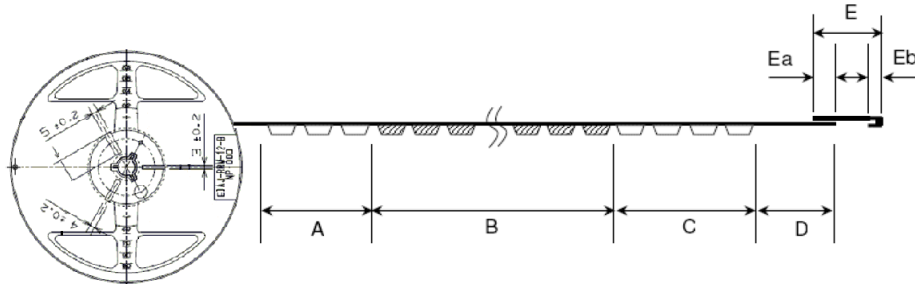


Figure 12 Tape & Reel Definition

Table 5 Taping Length

A	B	C	D	E
Ending pockets	Products pockets	Front pockets	Cover tape	Stop Tape
40mm min	1000pcs	100mm min	250mm min	50mm
		350mm min		70mm

Ea=20mm typ, Eb=10mm typ.

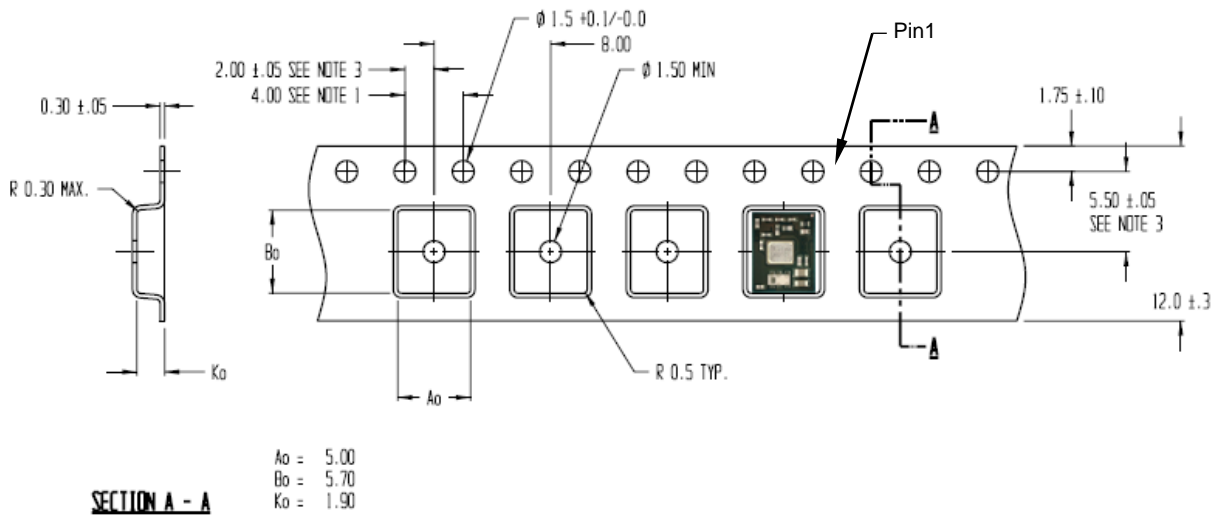


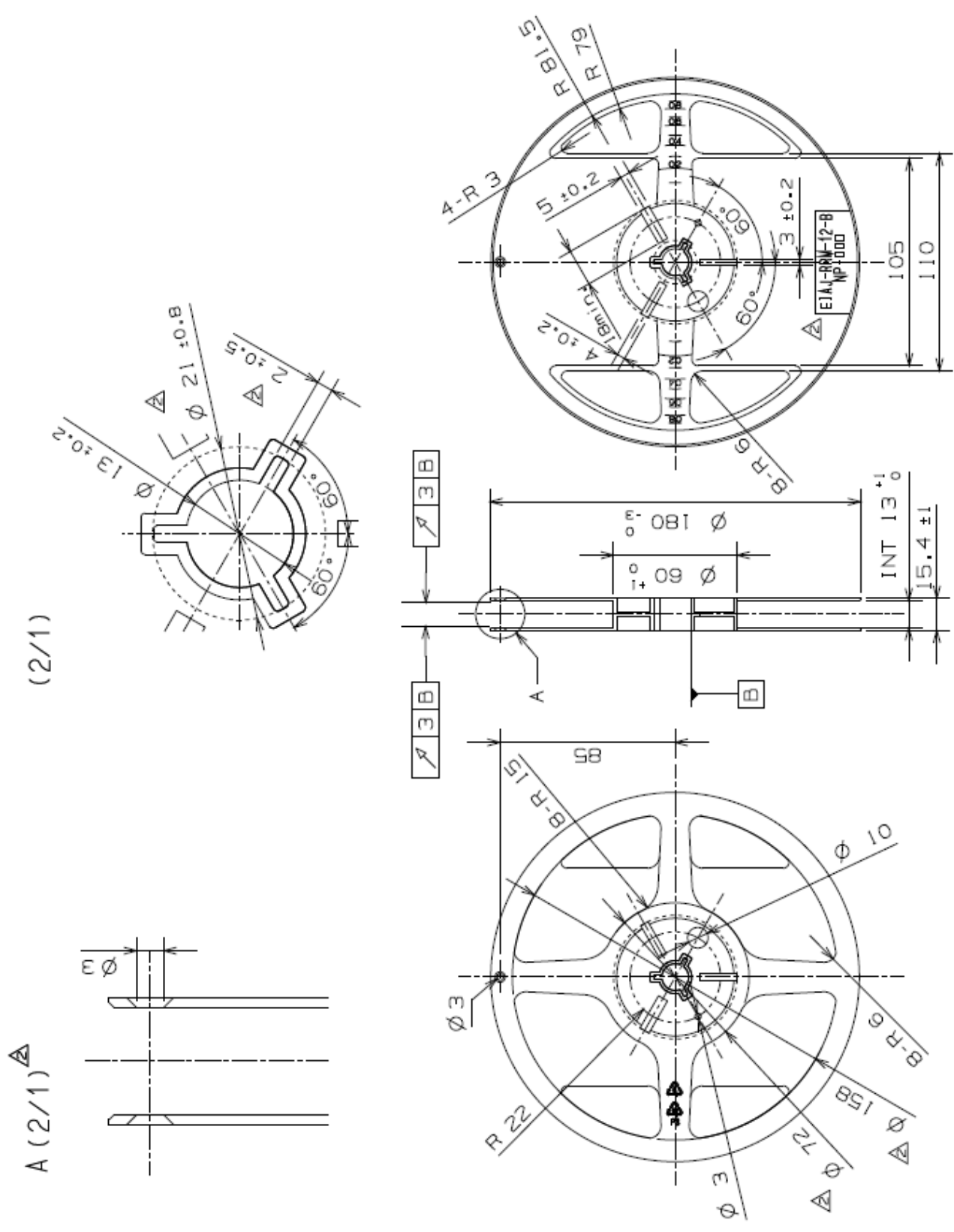
Figure 13 Carrier Tape Dimension

Notes:

- 1) 10 Sprocket hole pitches cumulative tolerance +/-0.2.
- 2) Pocket position relative to sprocket hole measured as true position, not pocket hole
- 3) Ao and Bo are calculated on a plane at a distance "R" above the bottom of the pocket.

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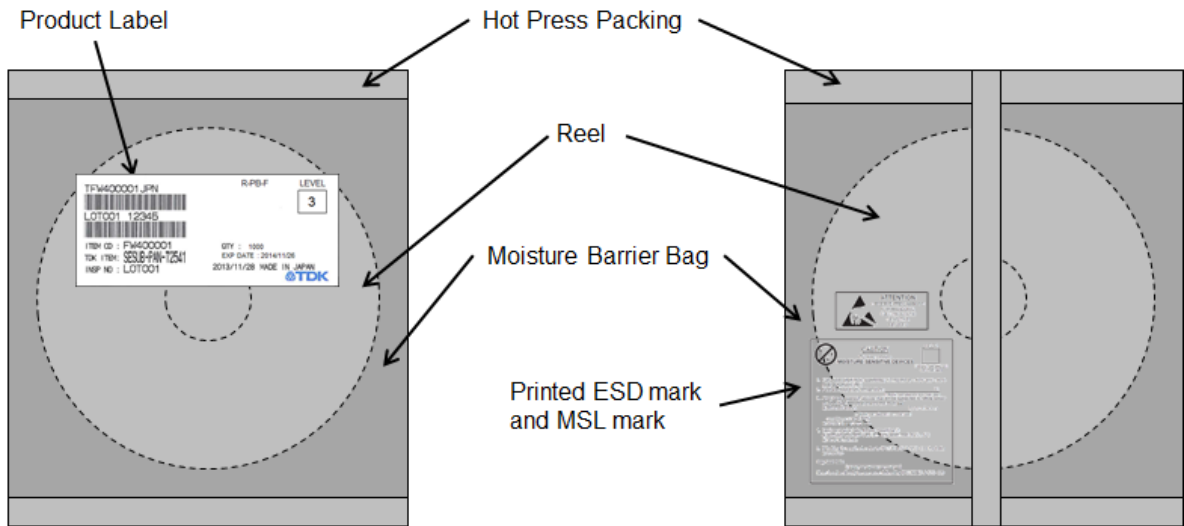
(2/1)

A (2/1)

Figure 14 Emboss Definition

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# Packing Label Specifications



**Figure 15 Packing Form**

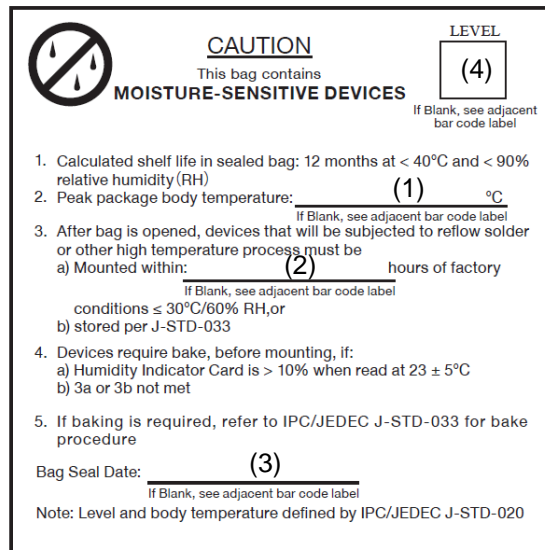
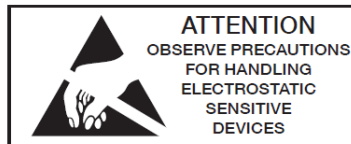
Storage Condition : 5-30°C / 40-60%RH

The guaranteed term until mounting is 12 months in the above condition.

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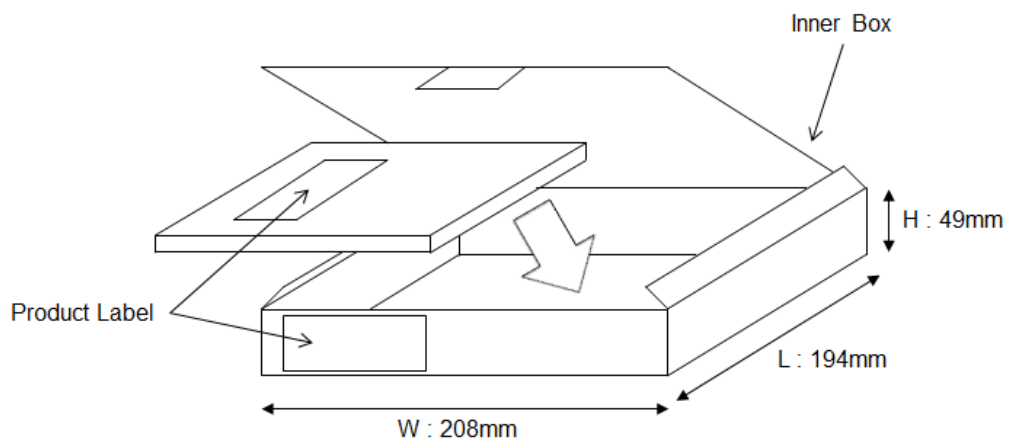
Figure 16 Product Label (Example)



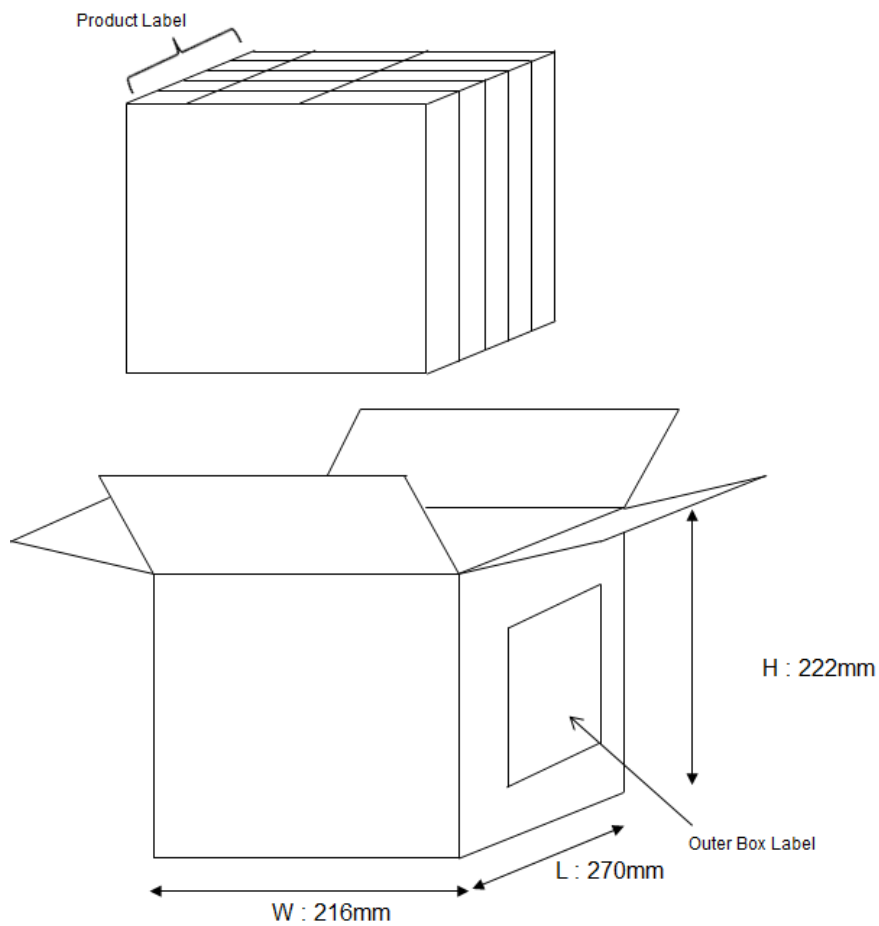
Items	Example
(1) Max Temperature	260
(2) Guarantee time after opening MBB	168
(3) Processing time	2013/11/27
(4) MSL Level	3

Figure 17 Packing Print (Example)

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**Figure 18 Inner Box**



**Figure 19 Outer Box**

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## Reference Information

1. Bluetooth Core Technical Specification document, version 4.0  
[http://www.bluetooth.com/SiteCollectionDocuments/Core\\_V40.zip](http://www.bluetooth.com/SiteCollectionDocuments/Core_V40.zip)
2. Texas Instruments CC2541 2.4GHz Bluetooth Low Energy System-on-Chip Datasheet (swrs110c)
3. Texas Instruments CC2541 System-on-Chip Solution for 2.4GHz Bluetooth low energy Applications User's Guide (swru191d)
4. Texas Instruments CC2540DK, Development Kit as of evaluation base board for SP13801  
<http://www.ti.com/tool/cc2540dk>
5. Texas Instruments SmartRF05 Evaluation Board User Guide (swru210a)  
<http://www.ti.com/lit/ug/swru210a/swru210a.pdf>
6. Texas Instruments CC2541EMK, CC2541 evaluation Module Kit
7. <http://www.ti.com/tool/cc2541emk>
8. Texas Instruments Bluetooth Low Energy Software Development Kit (SDK)  
<http://www.ti.com/tool/ble-stack>
9. Texas Instruments SmartRF Studio  
<http://focus.ti.com/docs/toolsw/folders/print/smarterfm-studio.html>
10. IAR Embedded Workbench for 8051 devices programming  
<http://www.iar.com>
11. For all other related technical documents, visit Texas Instruments Low-Power RF web site.  
<http://www.ti.com/lprf-forum>  
<http://www.ti.com/lprfnetwork>

## Lead-free Product Status

Conformity to RoHS Directive: This means that, in conformity with EU Directive 2002/95/EC, lead, cadmium, mercury, hexavalent chromium, and specific bromine-based flame retardants, PBB and PBDE, have not been used except for exempted applications.

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## Legal Description

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- (5) Atomic energy-related equipment
- (6) Seabed equipment
- (7) Transportation control equipment
- (8) Public information-processing equipment

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- (9) Military equipment
- (10) Electric heating apparatus, burning equipment
- (11) Disaster prevention/crime prevention equipment
- (12) Safety equipment
- (13) Other applications that are not considered general-purpose applications

When designing your equipment even for general-purpose applications, you are kindly requested to take into consideration securing protection circuit/device or providing backup circuits in your equipment.

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